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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Satoshi TAKANO

Application No.: 09/819,696 Docket No.: 109107

Filed: March 29, 2001

For: SEMICONDUCTOR MANUFACTURING METHOD, SUBSTRATE PROCESSING METHOD, AND SEMICONDUCTOR MANUFACTURING APPARATUS

REQUEST FOR CORRECTION OF PALM RECORDS

Director of the U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,

James A. Oliff
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Date: July 30, 2001

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/819,690	03/29/2001	2812	1000	109107	7	8	5

CONFIRMATION NO. 3148

25944
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277 S. WASHINGTON STREET, SUITE 500
ALEXANDRIA, VA 22314

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UPDATED FILING RECEIPT

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Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

Applicant(s)

Satoshi Takano, ~~Tokyo~~ → NAKANO-KU, JAPAN;

→ Assignment for Published Patent Application
HITACHI KOKUSAI ELECTRIC INC.

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2000-91642 03/29/2000
JAPAN 2001-60136 03/05/2001

If Required, Foreign Filing License Granted 05/05/2001

Projected Publication Date: 10/04/2001

N n-Publication Request: No

Early Publication Request: No

Title

Semiconductor manufacturing method, substrate processing method, and semiconductor manufacturing apparatus

Preliminary Class

438

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Bib Data Sheet

CONFIRMATION NO. 3148

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09/819,690	03/29/2001 RULE	438	2812	109107

APPLICANTS

Satoshi Takano, Nakand-Ku, JAPAN;

** CONTINUING DATA **********

** FOREIGN APPLICATIONS **********

JAPAN 2000-91642 03/29/2000
JAPAN 2001-60136 03/05/2001

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
** 05/05/2001

Foreign Priority claimed	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
☒ yes ☐ no	JAPAN	7	8	5
35 USC 119 (a-d) conditions met ☐ yes ☐ no ☐ Met after Allowance				
Verified and Acknowledged Examiner's Signature Initials				

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TITLE

Semiconductor manufacturing method, substrate processing method, and semiconductor manufacturing apparatus

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